Towards Operating System Support for Heterogeneous-ISA Platforms

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Emerging Hardware Trends



Fully **OS-capable Het-ISA** platforms



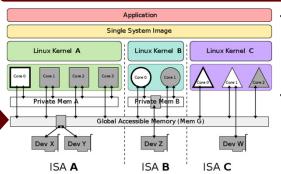
application application system in cpu0 cpu3 application application system i krn1 cpu0 cpu3 cpu application single system image

Asymmetric kernel OS

- **Explicit communication** in the application with a minimal kernel (e.g. NIX)
- Traditional Approach
- Explicit communication in the application (e.g. MPI. OpenCL)
- Multiple kernel OS
- Kernels communicate instead of the application (e.g. DVSM)

How to Run Applications?

The Popcorn OS Way



- to improve programmability

ISA platforms

OS concepts

- Single OS, multiple kernels
- Each kernel instance may run on a different ISA

Extends traditional SMP

- to support heterogeneous-

- Kernels communicate by message-passing
- A global OS state is maintained amongst kernels
- **Hiding hardware** diversity from apps
 - Applications run transparently across and amongst kernels

Kernels must coexist on the same hardware

- Kernels must be able to interact with any hardware resource
- Kernels communicate and coordinate
- Inter-kernel thread and process migration
- Single system image

Linux Prototype

Device Drivers

Processor core system wide id

Handling

Shared Memory

Peer kernels

Resource sharing amongst kernels

- To distinguish from previous approaches

- To take advantage of the mature software ecosystem

Kernels share system load

Adopt Linux kernel

Private and shared memory

allocation of private memory

Kernel to Kernel Communication

- Message passing
- Kernel level

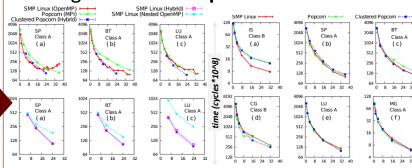
(User) Task Migration

- Migration service

Software Network Switch

- Each kernel has an IP address

Homogeneous-ISA Experimental Evaluation



Compute-bound NPB MPI/NEMESIS

An SMP OS kernel can be used as the base of a multiple kernel OS for emerging OS-capable heterogeneous-ISA platforms

